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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	Unknown
	Filing Date	Even Date Herewith
	First Named Inventor	Ryan, Kevin
	Group Art Unit	Unknown
	Examiner Name	Unknown
Sheet 1 of 1		Attorney Docket No: 303.539US3

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date (if Appropriate)
DN	US-5,748,635	05/05/1998	Thomann, M. , et al.	370	465	11/08/1996
	US-5,862,154	01/19/1999	Pawlowski, J. T.	371	40.11	01/03/1997
	US-5,909,701	06/01/1999	Jeddeloh, J. M.	711	167	07/18/1997
	US-5,926,838	07/20/1999	Jeddeloh, J. M.	711	167	03/19/1997
	US-5,933,859	08/03/1999	Klein, D. A.	711	211	04/25/1997
	US-5,935,263	08/10/1999	Keeth, B. , et al.	714	718	07/01/1997
	US-5,954,808	09/21/1999	Paul, K. A.	710	104	09/17/1997
	US-6,047,361	04/04/2000	Ingenio, G. V., et al.	711	167	08/21/1996
	US-6,108,730	08/22/2000	Dell, T. J., et al.	710	102	02/27/1998
DN	US-6,449,679	09/10/2002	Ryan, Kevin	710	315	02/26/1999

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DN		"Intel Offers SDRAM Alternative to Rambus on Camino Chipsets", Computergram International, (July 14, 1999), 1 p.	
		"SDRAM to Direct RDRAM", Intel Development Forum Presentation, Presentation Materials, (Undated),	
		CARROLL, M. , "Compaq, IBM, Others Seek Memory alternatives for Tanner- and Merced-class Systems -- Intel Preps Direct Rambus for Servers", Electronic Engineering Times, (Aug. 17, 1998), p. 1	
DN		HACHMAN, M. , et al., "Intel Simplifies Transition to New MPUs, Chipsets", Electronic Buyers News, (Dec. 7, 1998), p. 1	

EXAMINER

DAVID L. ROBERTSON
PRIMARY EXAMINER

DATE CONSIDERED

02/04/05

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached